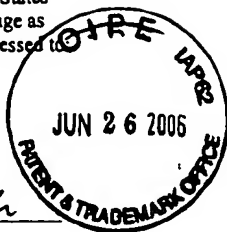


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Date: 22 June 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.	:	10/678,595
Applicant	:	Brian W. HUBER
Filed	:	03 October 2003
Titled	:	Voltage Regulator and Data Path for a Memory Device
Art Unit	:	2824
Examiner	:	Jung H. HUR
Atty. Docket No.	:	DB000859-007

RESPONSE UNDER RULE 116

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Sir:

In response to the Office action of 23 March 2006, please consider the following:

Remarks/Arguments begin on page 2 of this paper.